

Claims

- [1] A method of designing a micro-Base Transceiver Subsystem (BTS) of a CDMA system, including at least one channel card, at least one intermediate frequency (IF) board, a BTS control board, a digital combiner in a forward path of the BTS, a switching logic in a reverse path of the BTS, and a main board, said method comprising the step of embedding the digital combiner and the switching logic in the main board, wherein said main board acts as a backplane.
- [2] The method as claimed in claim 1, said method further comprising the step of embedding the BTS control board and the backplane in the main board, and wherein the digital combiner is embedded between the at least one channel card and the at least one IF board of the main board.
- [3] The method as claimed in claim 1, wherein I/Q data inputted from the forward path are combined to transmit serial data to individual channels of the at least one IF board, and wherein data inputted from the at least one IF board are transmitted via the switching logic in the reverse path.